1

MAR 1 2 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

plication of:

Duncan M. Walker, et al.

Serial No.:

09/997,786

Filing Date:

November 30, 2001

Group Art Unit:

2152

Title:

SYSTEM AND METHOD FOR DETECTING QUIESCENT

CURRENT IN AN INTEGRATED CIRCUIT

Confirmation No.

4798

Assistant Commissioner for Patents Washington, DC 20231

COPY OF PAPERS ORIGINALLY FILED

NOTIFICATION OF EXTENSION OF TIME UNDER 37 C.F.R. §1.136

Dear Sir:

Applicants hereby take an Extension of Time for responding to the Notice to File Missing Parts of Nonprovisional Application dated December 20, 2001, for one (1) month from February 20, 2002 to March 20, 2002.

A check in an amount of \$55.00 made payable to the Commissioner of Patents and Trademarks is attached hereto.

The Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

03/15/2002 SDENBOB1 00000058 09997786

02 FC:215

55.00 BP

Respectfully submitted, Attorneys for Applicants

Bradley P. Williams Reg/No. 40,227

Correspondence Address: Bradley P. Williams, Esq. Baker Botts L.L.P. 2001 Ross Avenue, Suite 600 Dallas, Texas 75201-2980

Tel: 214.953.6447 Fax: 214.661.4447

brad.williams@bakerbotts.com Date: February $\frac{17}{12}$, 2002

DAL01:659829.1